

#### Features:

- 650V Schottky Diode
- Zero Reverse Recovery Current
- High Frequency Operation
- Positive Temperature Coefficient
- Temperature independent
   Switching

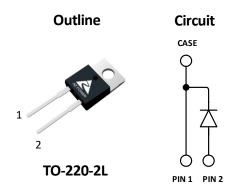
#### **Benefits:**

- Unipolar Rectifier
- Minimal switching loss
- Higher Efficiency
- Low cooling requirement

Symbol	Value	Unit
$V_{RRM}$	650	V
I <sub>F</sub> (Tc=154ºC)	8	А
Qc	28	nC

### **Applications:**

- Switch Mode Power Supply
- Booster diodes in PFC, DC/DC
- AC/DC converters



# **Maximum Ratings**

Symbol	Parameter	Value	Unit	Test Conditions
$V_R$	DC Peak Reverse Voltage	650	V	T <sub>J</sub> =25°C
V <sub>RRM</sub>	Repetitive Peak Reverse Voltage	650	V	T <sub>J</sub> =25°C
$V_{RSM}$	Surge Peak Reverse Voltage	650	V	T <sub>J</sub> =25°C
I <sub>F</sub>	Continuous Forward Current	26 12	Α	T <sub>C</sub> =25°C T <sub>C</sub> =135°C
''		8		T <sub>C</sub> =154°C
I <sub>FRM</sub>	Repetitive Peak Forward Surge Current	56 50	А	$T_{\text{C}}$ =25°C, $T_{\text{P}}$ =10ms, Half Sine Wave Tc=125°C, $T_{\text{P}}$ =10ms, Half Sine Wave
I <sub>FSM</sub>	Non-Repetitive Peak Forward Surge Current	74 67	А	$T_{C}$ =25°C, $T_{P}$ =10ms, Half Sine Wave Tc=125°C, $T_{P}$ =10ms, Half Sine Wave
P <sub>D</sub>	Power Dissipation	125 41.7	W	T <sub>C</sub> =25°C Tc=125°C
$T_{J,max}$	Operating Junction Temperature	175	°C	
$T_{stg}$	Storage Temperature Range	-55 to 175	°C	

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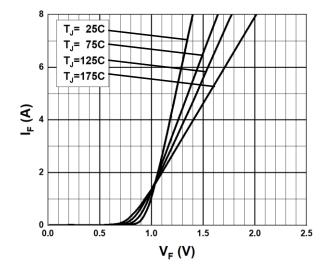
#### **Thermal characteristics**

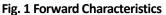
Symbol	Parameter	Min.	Тур.	Max.	Unit
$R_{thJC}$	Thermal Resistance		1.2		°C/W

### **Electrical Characteristics**

C. mah al	B	Value		l locit	Total Constitutions	
Symbol	Parameter	Min.	Тур.	Max.	Unit	Test Conditions
V <sub>DC</sub>	DC Blocking Voltage	650			V	I <sub>R</sub> =100μA, T <sub>J</sub> =25°C
\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	Forward Voltage		1.4	1.7	V	I <sub>F</sub> =8A, T <sub>J</sub> =25°C
V <sub>F</sub>	Forward Voltage		2.0	2.4	V	I <sub>F</sub> =8A, T <sub>J</sub> =175°C
	Daviera Comment		1	30		V <sub>R</sub> =650V, T <sub>J</sub> =25°C
I <sub>R</sub>	Reverse Current		10	100	μΑ	V <sub>R</sub> =650V, T <sub>J</sub> =175°C
	Tatal Canaciti o Chara		20		nC	I <sub>F</sub> =8A, dI/dt=400A/μs
Qc	Total Capacitive Charge		28			T <sub>J</sub> =25°C, V <sub>R</sub> =400V
			329			V <sub>R</sub> =1V, T <sub>J</sub> =25°C, f=1 MHz
С	Total Capacitance		45		pF	V <sub>R</sub> =200V, T <sub>J</sub> =25°C, f=1 MHz
			43			V <sub>R</sub> =400V, T <sub>J</sub> =25°C, f=1 MHz

# **Typical Performance**





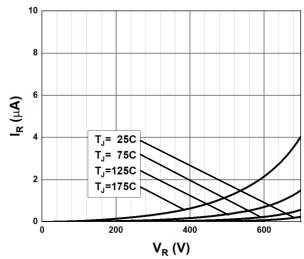
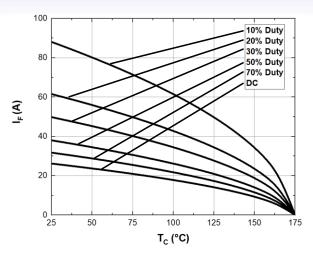


Fig. 2 Reverse Characteristics

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# **Typical Performance**



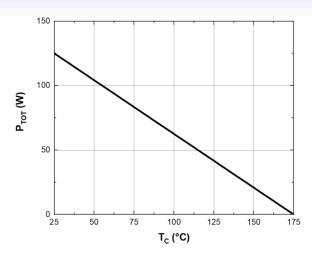


Fig. 3 Current Derating

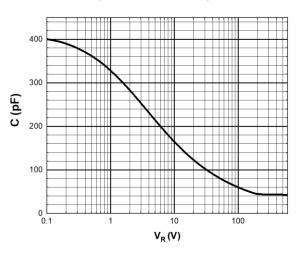


Fig. 4 Power Derating

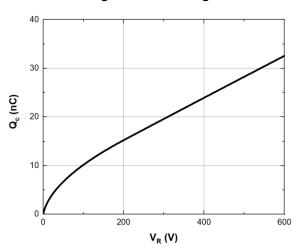


Fig. 5 Capacitance vs. Reverse Voltage

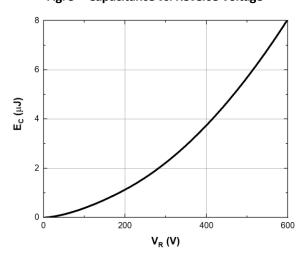


Fig. 6 Recovery Charge vs. Reverse Voltage

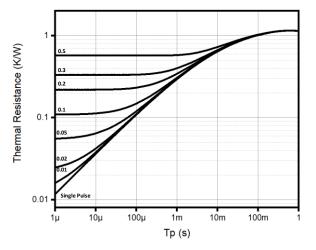


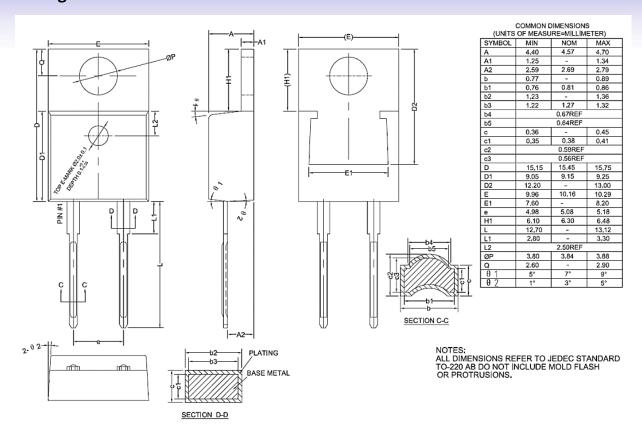
Fig. 7 Capacitance stored Energy

Fig. 7 Thermal Impedance

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Package TO-220-2L (Unit: mm)



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